Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (Electronics & Communication)

VI - SEMESTER(w.e.f. Batch 2018-19) 2018- 2022

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
							Theory			Practical			
				L	T	P	End Sem Unversity Exam	Two Term Exam	Teachers Asses-ment*	End Sem Unversity Exam	Teachers Asses-ment*		
1	BTEE503	Control System Engineering		3	1	2	60	20	20	30	20	150	5
2	BTEC601	Wireless Communication		3	0	0	60	20	20	0	0	100	3
3	BTEC602	Antenna and Microwave Engineering		3	1	2	60	20	20	30	20	150	5
4	BTEC603	Discrete Time Signal Processing		3	1	2	60	20	20	30	20	150	5
5	BTEC614/ BTEC624/ BTEI604	Electives: 1 Advanced Digital Design (HDL) 2 Television and RADAR Engineering 3 Digital image processing		3	1	2	60	20	20	30	20	150	5
6	BTEC605	Programming in Embedded C		0	0	2	0	0	0	60	40	100	1
TOTAL				15	4	10	300	100	100	180	120	800	24

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

* Students are advised to take online MOOCS course of minimum two credits

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, INDORE

Controller of Examinations
Shri Vaishnav Vidyapeeth Vishwavidyalay

Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore